

## MULTILAYER CERAMIC CHIP CAPACITOR(MLCC)

	No	Item	Test Method		Specification			
<u>32</u>	10	Resistance to Soldering Heat	Preheat the capacitor at $120^{\circ}\mathrm{C}$ for 1 minute. Have the capacitor dip into the solder bath at $270\pm5^{\circ}\mathrm{C}$ for $10\pm0.5$ second. Set it at room temperature( $25^{\circ}\mathrm{C}$ ) for $48\pm4$ hrs, them measure.  Initial measurement for X7R,X5R, Y5V,Z5U.Perform a heat treatment at $120\pm5^{\circ}\mathrm{C}$ for one minute and then set it at room temperature( $25^{\circ}\mathrm{C}$ ) for $48\pm4$ hrs.Perform the initial measurement.	Dielectric	NPO	X7R X5R	Y5V	Z5U
				Appearance	No defect			
				Capacitance Change	<±2.5% or ±0.25pF	±7.5%	±20%	±20%
				DF(or Q)	C≥30pF:Q≥1000 C≤30pF Q≥400+20C	as	Same as no.2	Same as no.2
				IR	More than 1G $\Omega$ or 50M $\Omega$ (Whichever is Smaller)			
				Dielectric Strength	No failure			
	11	Aging Rate	Preheat a heat treatment at 150±10°C for one hour and then set it at room temperature(25°C) for 48±4hrs. Perform the initial measurement. After 1000 hours perform the final measurement.	Capacitance Change	NPO≤1% / decade hour X7R/X5R≤3% / decade hour Y5V≤5% / decade hour Z5U≤5% / decade hour			
				DF	Same as no.2			
	12	High Temperature Load	Applied 200% of the rate voltage for 1000 $\pm$ 12 hours at the maximum operating temperature $\pm$ 3°C. Let sit for 24 $\pm$ 2 hours(NPO) or 48 $\pm$ 4 hours(X7R,X5R, Y5V,Z5U)at room temperature(25°C), then measure. High Voltage Serier (500V~4KV) Applied 150% of the rated voltage for 500V; 125% of the rated voltage for 1~4KV/Y2/Y3 for 1000 $\pm$ 12hours at the maximum operating temperature $\pm$ 3°C. Let sit for 24 $\pm$ 2hours (NPO) or 48 $\pm$ hours (X7R,X5R,Y5V,Z5U) at room temperture. Them measure.	Dielectric	NPO	X7R X5R	Y5	//Z5U
				Appearnce	No defect			
				Capacitance Change	< $\pm$ 3% or $\pm$ 0.3pF	238004 0 2 100 0		30%
				DF(or Q)	C>30pF: Q ≥350 10pF≤ C ≤350pF Q≥275+5C/2 C<10pF: Q≥200+10C	Vr=50V~4I DF≤7.0% Vr=25V/16 DF≤11% Vr=10V DF≤13% Vr=6.3V Df≤15%	10° V Vr= 13° Vr= 16° Vr=	≥25V, 6,max 16V, 6,max 10V, 6,max 6.3V ≤18.0%
				IR	More than 1G $\Omega$ or 50M $\Omega$ (Whichever is Smaller)			
				Dielectric Stregth	No failure			
	13	Humidity, Steady State	Set the capacitor at $40\pm2^{\circ}\!$	Dielectric	NPO	X7R X5R	Y51	//Z5U
				Apperance	0. CMW 141	defect		
				Capacitance Change	< $\pm$ 5% or $\pm$ 0.5pF	±12.5%	6 ±	30%
				DF(or Q)	C>30pF: Q ≥350 10pF≤ C ≤350pF Q≥275+5C/2 C<10pF: Q≥200+10C	Vr=50V~4I DF≤7.0% Vr=25V/16 DF≤111% Vr=10V DF≤13% Vr=6.3V Df≤15%	10° V Vr= 13° Vr= 16° Vr=	≥25V, 6,max 16V, 6,max 10V, 6,max 6.3V ≤18.0%
				IR	More than 1G $\Omega$ or $50\Omega$ F (Whichever is Smaller)			
				Dielectric Stregth	No failure			
	14	Storage	Store all capacitors indoors at temperature of 5~35°C. Humidity<75%.  They are warranted for a period of 2 years from the date of manufacture.					