MULTILAYER CERAMIC CHIP CAPACITOR(MLCC)



Specfication and test methods

No	lt e m	Test Method	Specification
1	Capacitance	The capacitance/DF shall be measured at 25°C at the frequency and voltage shown below	Within the specifide tolerance
2	Dissipation Factor	Type NPO(\leq 1nf) NPO($>$ 1nf) X7R,X5R,Y5V,Z5U Frequency 1 \pm 0.1MHz 1 \pm 0.1KHz Voltage 1 \pm 0.2Vrms $0.1\pm$ 0.02Vrms (Y5V)	$\begin{tabular}{llll} NPO:DF & 0.1\% \\ X7R/X5R:Vr = 50V & 4KV \\ DF & & 5\% \\ Vr = 25V, & DF & 7\% \\ Vr = 16V, & DF & 9\% \\ Vr = 10V, & DF & & 11\% \\ Vr = 6.3V, & DF & & 13\% \\ Y5V:Vr = 25V/50V, & DF & & 8\% \\ Vr = 16V, & DF & & 10\% \\ Vr = 6.3V/10V, & DF & & 13\% \\ Z5U:Vr = 100V/250V, & DF & & 5\% \\ \end{tabular}$
3	Insulation Resistance	Test voltage:rated voltage Charge time:2 minutes max. Charge current:less than 50mA	$\begin{array}{lll} NPO:{\geqslant}10G\Omega & Y5V:{\geqslant}1G\Omega \\ X7R:{\geqslant}5G\Omega & Z5U:{\geqslant}1G\Omega \\ X5R:{\geqslant}3G\Omega & \end{array}$
4	Dielectric Strength	Test voltage(Vt): $Vt=Vr\times250\% \text{ for NPO}(Vr=25V/50V/100V)$ $Vt=Vr\times250\% \text{ for X7R},X5R,Y5V/Z5U} (Vr=10V/16V/25V/50V/100V)$ $Vt=Vr\times150\%+100 \text{ for product Vr}=250V$ $Vt=Vr\times150\% \text{ for product Vr}=500V$ $Vt=Vr\times120\% \text{ for product Vr}=1KV\sim4KV$ $Y2:2500VAC/5KVDC-250VAC$ $Y3:1500VAC/3KVDC-250VAC$ $Test time:1\sim5 \text{ seconds}$ $Charge current:less than 50mA$	No defect or abnormality Ir ≤ 1uA
5	Breakdown Voltage	The DC voltage is applied between the terminations until the leakage current more than 10uA	Vr=10V/16V/25V/50V, BDV≥700%x Vr Vr=100V/200V/250V. BDV≥500%x Vr Vr>250V BDV≥300%x Vr Vr>1KV BDV≥200%x Vr Vr≥3KV BDV≥150%x Vr Y2:3000VAC/6KVDC Y3:2000VAC/4KVDC
6	Appearance	X8 magnification glass	No defect or abnormalities
7	Dimension	Using calipers	Within the specified dimension
8	Solder ability of Termination	The capacitors are dipped into the solder bath at $225\pm5\%$ for 2 ± 0.5 seconds, then check the soldering by measuring the areas cover with solder.	95% of the terminations is to be soldered evenly and continuously.
9	Leaching (endurance of Ni to soldering heat)	The capacitors are dipped into the solder bath at $260\pm5^\circ\!$	95% of the terminations are to be soldered evenly and continuously.

32